TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

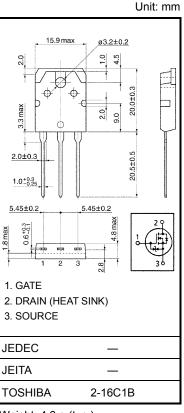
2SK3314

Chopper Regulator and DC–DC Converter Applications Motor Drive Applications

- Fast reverse recovery time $t_{rr} = 105 \text{ ns} (typ.)$
- Built-in high-speed free-wheeling diode
- Low drain-source ON resistance $RDS(ON) = 0.35 \Omega$ (typ.)
- High forward transfer admittance $|Y_{fs}| = 9.9 \text{ S (typ.)}$
- Low leakage current $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 500 \ V)$
- Enhancement mode $: V_{th} = 2.0 \sim 4.0 \text{ V} (V_{DS} = 10 \text{ V}, \text{ID} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	500	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	500	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	15	А	
	Pulse (Note 1)	I _{DP}	60	А	
Drain power dissipation (Tc = 25° C)		PD	150	W	
Single pulse avalanche energy (Note 2)		E _{AS}	630	mJ	
Avalanche current		I _{AR}	15	А	
Repetitive avalanche energy (Note 3)		E _{AR}	15	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.833	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	50	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 4.76 mH, R_G = 25 Ω , I_{AR} = 15 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature.

This transistor is an electrostaticsensitive device. Please handle with caution.

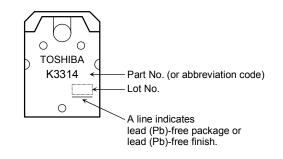
Electrical Characteristics (Ta = 25°C)

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μA
Gate-source br	eakdown voltage	V (BR) GSS	I _G = ±100 μA, V _{DS} = 0 V	±30	_		V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V	_	—	100	μA
Drain-source bi	reakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	500	_	_	V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 7 A	_	0.35	0.49	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 7 A	5.0	9.9	_	S
Input capacitand	but capacitance C _{iss}			_	2600	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	280	_	
Output capacitance		C _{oss}			880	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{}_{0V} \stackrel{I_{D}=7A}{}_{0V} \stackrel{V_{OUT}}{}_{KL} = 30\Omega$		50	_	
	Turn-on time	t _{on}			85		
	Fall time	t _f		_	65	_	ns
	Turn-off time	t _{off}	$v_{DD} \rightarrow 210v$ Duty $\leq 1\%$, $t_w = 10\mu s$		260	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	58	_	nC
Gate-source charge		Q _{gs}	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 15 A	_	36	—	
Gate-drain ("miller") charge		Q _{gd}	1		22	_	1

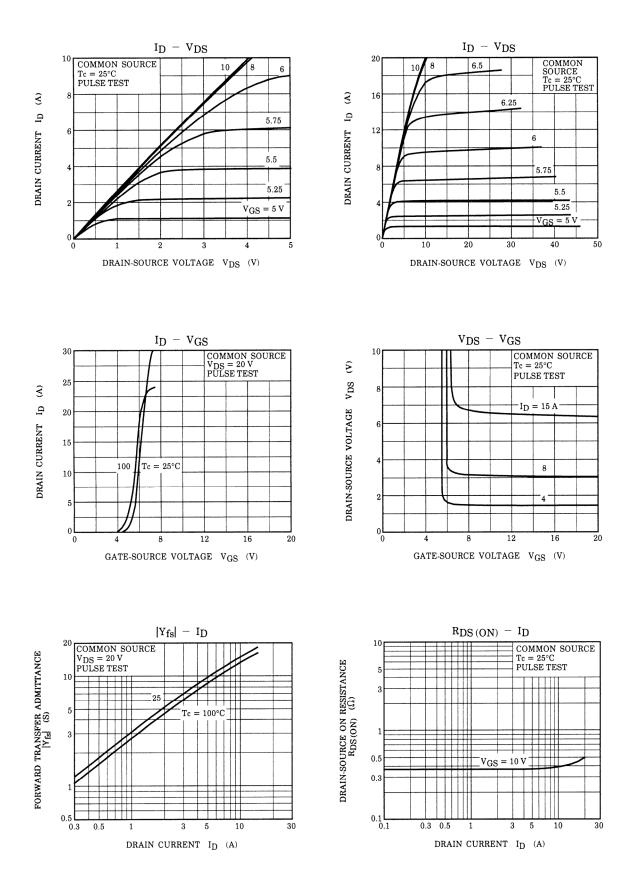
Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	15	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	60	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 15 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 15 A, V _{GS} = 0 V dI _{DR} / dt = 100 A / μs	_	105	180	ns
Reverse recovery charge	Q _{rr}		_	0.24	_	μC

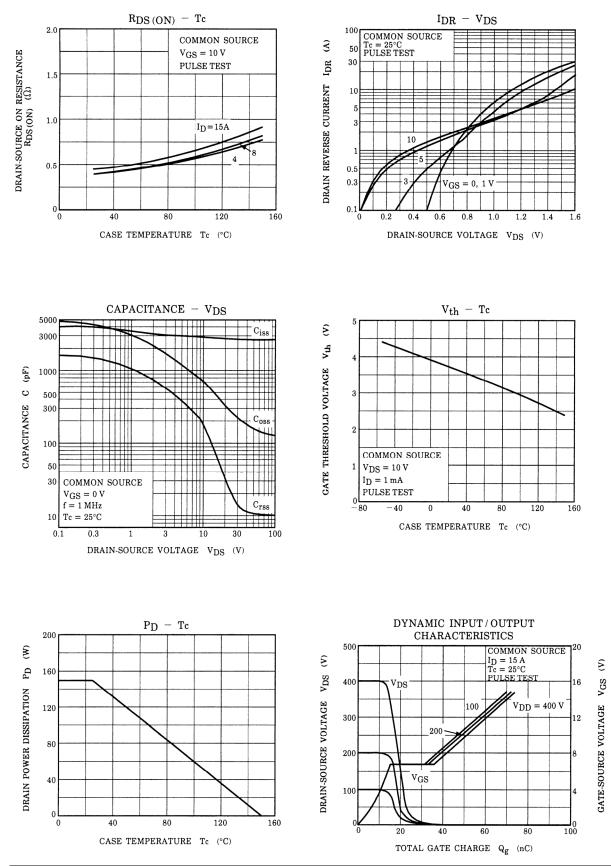
Marking

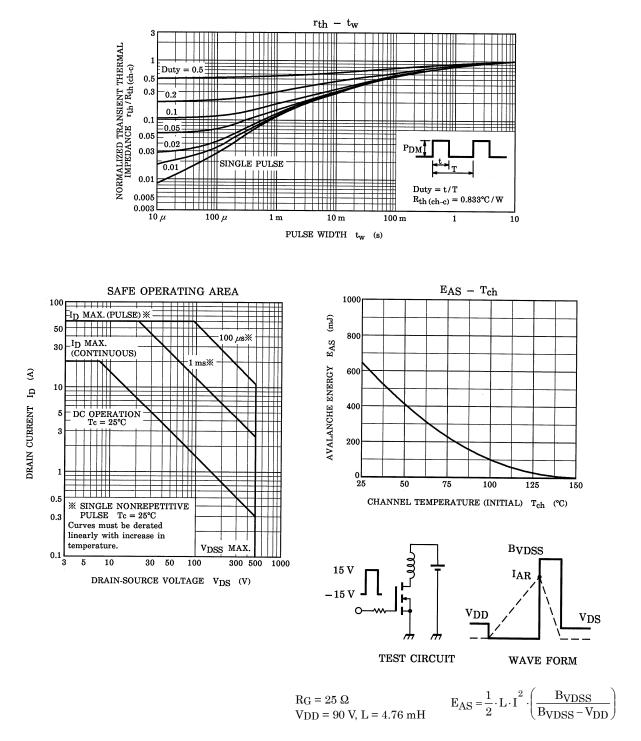


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